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**Facto HoD & Assistant Professor-
Computer Science & Engineering**
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Profile

A Computer Science academician and academic administrator with fifteen years of experience in teaching, research and academic leadership, complemented by one year of industry experience. Over this period, a strong record has been built across CSE education, department administration, research supervision, laboratory management, placement coordination and technical skill development. The candidate has published more than thirteen research papers, guided nine postgraduate students and supervised over twenty undergraduate projects, reflecting a sustained commitment to both classroom teaching and applied research.

Core areas of expertise span Networking, Programming, Software Testing and Selenium Automation, alongside Cryptography and Network Security, Blockchain and AI/ML. This is supported by strong foundations in Java, C/C++, Database Management Systems and Data Structures, enabling both theoretical instruction and hands-on laboratory and project guidance across these domains.

The candidate is currently pursuing a Ph.D. in Computer Science Engineering at Manipal University, Jaipur, since 2025. An M.Tech. in Computer Science Engineering was completed at Jagannath University, Jaipur, in 2012, and a B.E. in Computer Science Engineering was completed at Rajasthan University, Jaipur, in 2007.

The candidate is currently serving at ICFAI University, Jaipur, as Facto Head (CSE) and Assistant Professor since January 2024. Prior to this, from August 2017 to January 2024, the role of Facto Head (SCIT) and Assistant Professor was held at Anand International College of Engineering, Jaipur. Between July 2011 and June 2017, the candidate worked as M-Tech Coordinator and Assistant Professor at Jagannath University, Jaipur. Earlier still, from July 2008 to June 2011, the position of Facto Head (CSE) and Assistant Professor was held at Poomima Group of College, Rajasthan. The candidate's career began in industry as a Junior Developer (Java) at Eware Technologies, Gurgaon, from July 2007 to June 2008.

Across these roles, the candidate has served as Facto HOD/Head (CSE/SCIT) and as M-Tech (CSE/IT) Programme Coordinator, taking overall responsibility for laboratory and timetable coordination as well as academic, placement and departmental administration. The candidate has also served as a member of Discipline and Admission Committees, working closely with faculty and students in these capacities.

The candidate's M.Tech. thesis examined reliability, chunk authentication and dynamic address reconfiguration in SCTP. Building on this foundation, more than thirteen research

papers have been published in international peer-reviewed journals and conferences. The candidate has guided nine postgraduate research students and more than twenty undergraduate projects, and has attended over twenty-five professional development activities while organizing three workshops and guest lectures.

Research interests include Networking, MANET/WSN routing, network security, cryptography, steganography, image processing, Hadoop and Big Data, Bluetooth/RFID, software testing, and other emerging computing technologies.

Notable publications include "Energy Efficient Routing in MANETs Using Multi Objective Genetic Algorithm" (IJSETR, 2020), "Gray Scale Image Compression" (IJARCCE, 2016), "Denial of Services Attack and Its Prevention" (IJSET, 2015), "Map Reducing in Big Data using Hadoop" (IJCS, 2015), "Address Spoofing in IPv6" (IJSET, 2015), "Secure Steganography Using Discrete Wavelet Transformation" (IJEMS, 2015), "Energy Efficient Routing in MANETs: A Survey" (IJRITCC, 2015), "Image Watermarking Using LSB Technique" (IJERGS, 2015), "SCTP Reliability, Chunk Authentication and Dynamic Address Reconfiguration" (IJSER, 2015), "Wireless Security Using Image Encryption and Decryption" (IJSET, 2014), "Comparative Analysis of RSA and MD5 Algorithms" (JCSA, 2014), and "Improved Bluetooth Connectivity With RFID" (IJIET, 2013).

Projects supervised include 3-D Object Reconstruction Comparing Two Stereo Images; Dual Security Using Blowfish and Watermarking; Energy Efficient Protocols for Wireless Sensor Networks; Energy Efficient Routing in MANETs Using Multi Objective Genetic Algorithm; Denial of Service and Network Reconfiguration Using Spoofing; and Big Data MapReduce Using Hadoop with Packet-Dropping Detection in Wireless Sensor Networks.

The candidate received the Best Teacher Award at Poornima Group of College in 2009, and was Winner of the First Level of the IBM Great Mind Challenge in 2006, having earlier participated in the IBM I.N.V.I.T.E Software Development Competition in 2005. In 2022, the candidate served as Session Chair at IndiaCom, the International Conference on Recent Developments in Engineering and Technology. The candidate has undergone Faculty Development Programme and refresher exposure in AI, AI/ML, Cloud Computing, IPR, Cyber Security, Blockchain, Python, LaTeX and Human Values, and has attended workshops in Generative AI, Cryptography, Software Testing, IoT, Edge/Cloud Computing and Blockchain. Workshops have also been organized on IoT, IT Infrastructure and Project Management, and Software Engineering. Certification achievements include IBM RAT with 94.4%, IBM DB2 with 90.1%, Red Hat Networking (RHCE) with 86%, and MCP with 88.11%.